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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/541,771	04/03/2000	John R. McVey	10991816-1	4745

22879 7590 07/07/2003

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EXAMINER

LAMB, TWYLER MARIE

ART UNIT PAPER NUMBER

2622

DATE MAILED: 07/07/2003

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/541,771

Applicant(s)

MCVEY ET AL.

Examiner

Twyler M. Lamb

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 April 2000.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 16-21 is/are allowed.
- 6) ☒ Claim(s) 1-12 is/are rejected.
- 7) ☒ Claim(s) 13-15 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2.
- 4) ☐ Interview Summary (PTO-413) Paper No(s) _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Specification

1. The abstract of the disclosure is objected to because it exceeds 150 words.
Correction is required. See MPEP § 608.01(b).

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-6 and 10-11 are rejected under 35 U.S.C. 102(b) as being anticipated by Pawlowski et al. (Pawlowski) (US 5,812,803).

With regard to claims 1 and 10, Pawlowski discloses a method for performing a transaction on a bus (see Abstract), comprising: receiving a signal requesting the transaction (col 15, lines 45-48); generating a first value using the signal (col 15, line 64 – col 16, line 1); storing the first value in a storage device, with the first value including a plurality of bits indicating a beginning of usage of the bus and an ending of the usage of the bus for the transaction in terms of clock cycles (col 14, lines 44-64); and executing the transaction according to the first value (col 14, lines 44-64).

With regard to claims 2 and 11, Pawlowski also discloses storing the first value in the storage device includes storing the plurality of bits in storage elements included in the storage device, with those of the plurality of bits in a first state indicating the clock cycles during which the usage of the bus occurs for the transaction (col 14, lines 44-64).

With regard to claim 3, Pawlowski also discloses each of the storage elements stores one of the plurality of bits (col 14, lines 44-64).

With regard to claim 4, Pawlowski also discloses receiving the signal includes receiving a second value indicating a number of the clock cycles during which the usage of the bus occurs for the transaction (col 16, lines 1-3); generating the first value includes generating the plurality of bits using the second value with positions within the first value of those of the plurality of bits in the first state indicating the clock cycles during which the usage of the bus occurs for the transaction (col 15, line 45 – col 16, line 3); and each of the positions within the first value corresponds to one of the storage elements (col 14, lines 44-64).

With regard to claim 5, Pawlowski also discloses changing the first value in the storage device after storing in the first value and after an occurrence of at least one of the clock cycles by shifting ones of the plurality of bits between the storage elements (col 14, lines 9-18).

With regard to claim 6, Pawlowski also discloses executing the transaction includes monitoring a first one of the positions to determine a beginning of the transaction (col 14, lines 9-18).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

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invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 7-9 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over XX in view of Sarangdhar et al. (Sarangdhar) (US 5,581,782).

With regard to claims 7 and 12, Pawlowski differs from claim 7 in that he does not teach those of the plurality of bits in a second state indicate the clock cycles during which the bus exists in an idle condition; and generating the first value includes generating the plurality of bits in the second state so that the bus exists in the idle condition for at least one of the clock cycles between the usage of the bus for the transaction and the usage of the bus for a previous transaction.

Sarangdhar discloses a system that includes those of the plurality of bits in a second state indicate the clock cycles during which the bus exists in an idle condition (col 2, line 63 – col 3, line 17); and generating the first value includes generating the plurality of bits in the second state so that the bus exists in the idle condition for at least one of the clock cycles between the usage of the bus for the transaction and the usage of the bus for a previous transaction (col 2, line 63 – col 3, line 17).

Therefore it would have been obvious to one of ordinary skill in the art at the time of the invention to have modified Pawlowski to include those of the plurality of bits in a second state indicate the clock cycles during which the bus exists in an idle condition; and generating the first value includes generating the plurality of bits in the second state so that the bus exists in the idle condition for at least one of the clock cycles between the usage of the bus for the transaction and the usage of the bus for a previous transaction as taught by Sarangdhar. It would have been obvious to one of ordinary

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skill in the art at the time of the invention to have modified Pawlowski by the teaching of Sarangdhar to provide high performance symmetric arbitration protocol that includes support for priority agents and distributed arbitration by indicating which agent is busy or idle as taught by Sarangdhar in col 2, lines 37-46.

With regard to claim 8, Pawlowski as modified also discloses the bus including a data bus (col 4, lines 13-15); the transaction includes an access to a memory device including a control phase and a data phase (col 7, lines 1-4); executing the transaction includes beginning the control phase when the first one of the positions enters the second state (col 7, lines 1-47); and executing the transaction includes beginning the data phase when a second one of the positions enters the first state (col 7, lines 1-47).

With regard to claim 9, Pawlowski as modified also discloses the bus including an address bus (col 5, lines 16-28); the transaction includes an access to a memory device including a control phase (col 5, lines 16-28); executing the transaction includes beginning the control phase when the first one of the positions enters the first state (col 5, lines 16-28).

Allowable Subject Matter

6. Claims 13-15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

7. Claims 16-21 are allowed.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Twyler Lamb whose telephone number is 703 - 308-8823. The examiner can normally be reached on M-TH (8:30-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward L Coles can be reached on 703-308-4712. The fax phone numbers for the organization where this application or proceeding is assigned are 703-746-6036 for regular communications and 703-872-9314 for After Final communications.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, DC 20231

or faxed to:

(703) 872-9314

(for informal or draft communications, such as proposed amendments to be discussed at an interview; please label such communications "PROPOSED" or "DRAFT")

or hand-carried to:

Crystal Park Two

2121 Crystal Drive

Arlington, VA.

Sixth Floor (Receptionist)

Twyler Lamb



June 28, 2003